



BEST AVAILABLE COPY

**PATENT APPLICATION**

**RESPONSE UNDER 37 CFR §1.116  
EXPEDITED PROCEDURE  
TECHNOLOGY CENTER ART UNIT 2822**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of

Nobuaki HASHIMOTO

Group Art Unit: 2822

Application No.: 09/856,627

Examiner: I. Soward

Filed: May 24, 2001

Docket No.: 109609

For: INTERCONNECT SUBSTRATE, SEMICONDUCTOR DEVICE, METHODS OF  
FABRICATING, INSPECTING, AND MOUNTING THE SEMICONDUCTOR  
DEVICE, CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT

**AMENDMENT AFTER FINAL REJECTION UNDER 37 CFR §1.116**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In reply to the September 9, 2003 Office Action, please consider the following:

**Amendments to the Claims** as reflected in the listing of claims; and

**Remarks.**